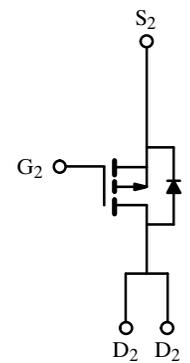
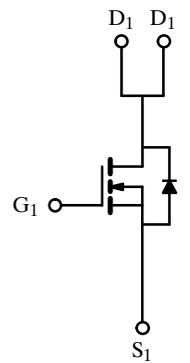
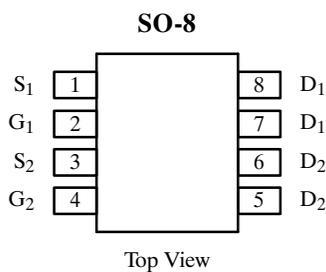


Dual Enhancement-Mode MOSFETs (N- and P-Channel)

Product Summary

	V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A)
N-Channel	30	0.05 @ $V_{GS} = 10$ V	± 3.5
		0.07 @ $V_{GS} = 6$ V	± 3
		0.08 @ $V_{GS} = 4.5$ V	± 2.5
P-Channel	-30	0.10 @ $V_{GS} = -10$ V	± 3.5
		0.12 @ $V_{GS} = -6$ V	± 3
		0.16 @ $V_{GS} = -4.5$ V	± 2.5



Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

Parameter	Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage	V_{DS}	30	-30	V
Gate-Source Voltage	V_{GS}	± 20	± 20	V
Continuous Drain Current ($T_J = 150^\circ\text{C}$) ^a	I_D	$T_A = 25^\circ\text{C}$	± 3.5	A
		$T_A = 70^\circ\text{C}$	± 2.8	
Pulsed Drain Current	I_{DM}	± 14	± 14	A
Continuous Source Current (Diode Conduction) ^a	I_S	1.7	-1.7	A
Maximum Power Dissipation ^a	P_D	$T_A = 25^\circ\text{C}$	2.0	W
		$T_A = 70^\circ\text{C}$	1.3	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 to 150		$^\circ\text{C}$

Thermal Resistance Ratings

Parameter	Symbol	N- or P- Channel	Unit
Maximum Junction-to-Ambient ^a	R_{thJA}	62.5	$^\circ\text{C}/\text{W}$

Notes

a. Surface Mounted on FR4 Board, $t \leq 10$ sec.

Subsequent updates to this data sheet may be obtained via facsimile by calling Siliconix FaxBack, 1-408-970-5600. Please request FaxBack document #1228.

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Specifications ($T_J = 25^\circ\text{C}$ Unless Otherwise Noted)

Parameter	Symbol	Test Condition	Min	Typ ^a	Max	Unit	
Static							
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	N-Ch	1.0		V	
		$V_{DS} = V_{GS}, I_D = -250 \mu\text{A}$	P-Ch	-1.0			
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}$	N-Ch		1	μA	
		$V_{DS} = -24 \text{ V}, V_{GS} = 0 \text{ V}$	P-Ch		-1		
		$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 70^\circ\text{C}$	N-Ch		5		
		$V_{DS} = -15 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 70^\circ\text{C}$	P-Ch		-5		
On-State Drain Current ^b	$I_{D(on)}$	$V_{DS} \geq 5 \text{ V}, V_{GS} = 10 \text{ V}$	N-Ch	14		A	
		$V_{DS} \leq -5 \text{ V}, V_{GS} = -10 \text{ V}$	P-Ch	-14			
		$V_{DS} \geq 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	N-Ch	3.5			
		$V_{DS} \leq -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	P-Ch	-2.5			
Drain-Source On-State Resistance ^b	$r_{DS(on)}$	$V_{GS} = 10 \text{ V}, I_D = 3.5 \text{ A}$	N-Ch		0.04	0.05	Ω
		$V_{GS} = -10 \text{ V}, I_D = 3.5 \text{ A}$	P-Ch		0.08	0.10	
		$V_{GS} = 6 \text{ V}, I_D = 3 \text{ A}$	N-Ch		0.05	0.07	
		$V_{GS} = -6 \text{ V}, I_D = 3 \text{ A}$	P-Ch		0.10	0.12	
		$V_{GS} = 4.5 \text{ V}, I_D = 2.5 \text{ A}$	N-Ch		0.06	0.08	
		$V_{GS} = -4.5 \text{ V}, I_D = 2 \text{ A}$	P-Ch		0.13	0.16	
Forward Transconductance ^b	g_{fs}	$V_{DS} = 15 \text{ V}, I_D = 3.5 \text{ A}$	N-Ch		8	S	
		$V_{DS} = -15 \text{ V}, I_D = -3.5 \text{ A}$	P-Ch		5		
Diode Forward Voltage ^b	V_{SD}	$I_S = 1.7 \text{ A}, V_{GS} = 0 \text{ V}$	N-Ch		0.80	1.2	V
		$I_S = -1.7 \text{ A}, V_{GS} = 0 \text{ V}$	P-Ch		-0.80	-1.2	
Dynamic^a							
Total Gate Charge	Q_g	N-Channel $V_{DS} = 10 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 3.5 \text{ A}$ P-Channel $V_{DS} = -10 \text{ V}, V_{GS} = -10 \text{ V}$ $I_D = -3.5 \text{ A}$	N-Ch		13	35	nC
Gate-Source Charge	Q_{gs}		N-Ch		2.1		
Gate-Drain Charge	Q_{gd}		N-Ch		5.3		
Turn-On Delay Time	$t_{d(on)}$	N-Channel $V_{DD} = 15 \text{ V}, R_L = 15 \Omega$ $I_D \cong 1 \text{ A}, V_{GEN} = 10 \text{ V}, R_G = 6 \Omega$ P-Channel $V_{DD} = -15 \text{ V}, R_L = 15 \Omega$ $I_D \cong -1 \text{ A}, V_{GEN} = -10 \text{ V}, R_G = 6 \Omega$	N-Ch		11	30	ns
Rise Time	t_r		N-Ch		17	40	
			P-Ch		21	40	
Turn-Off Delay Time	$t_{d(off)}$		N-Ch		26	50	
			P-Ch		24	50	
Fall Time	t_f		N-Ch		11	50	
			P-Ch		9	50	
Source-Drain Reverse Recovery Time	t_{rr}		$I_F = 3.5 \text{ A}, di/dt = 100 \text{ A}/\mu\text{s}$	N-Ch		70	
			P-Ch		40	100	

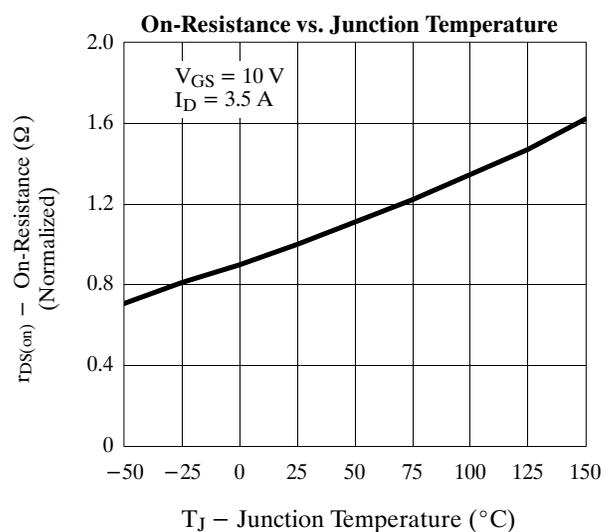
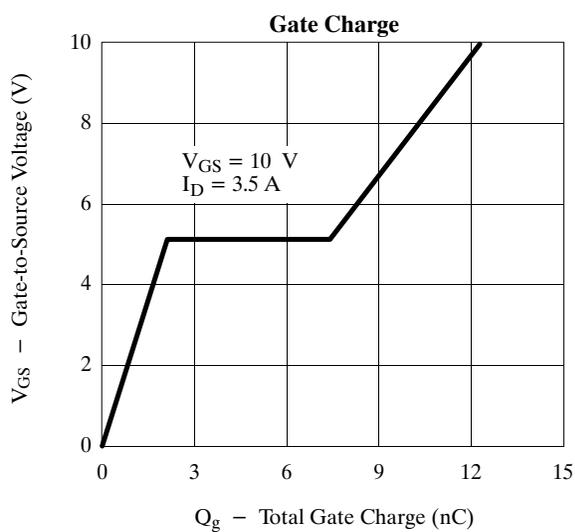
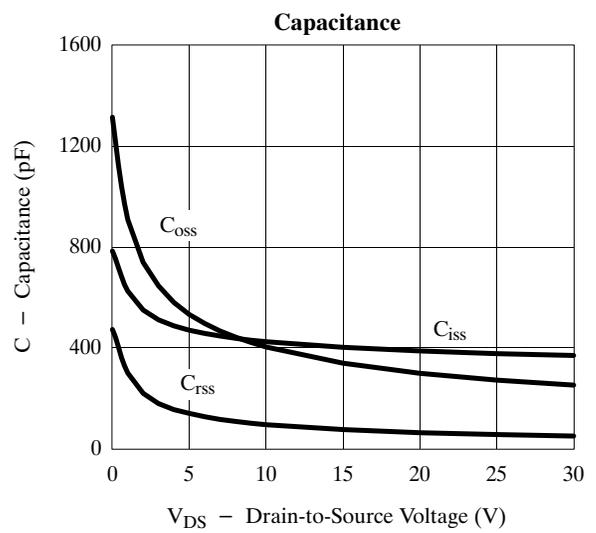
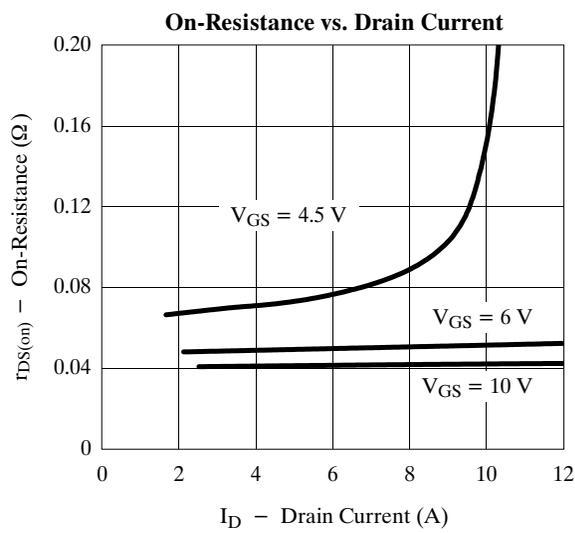
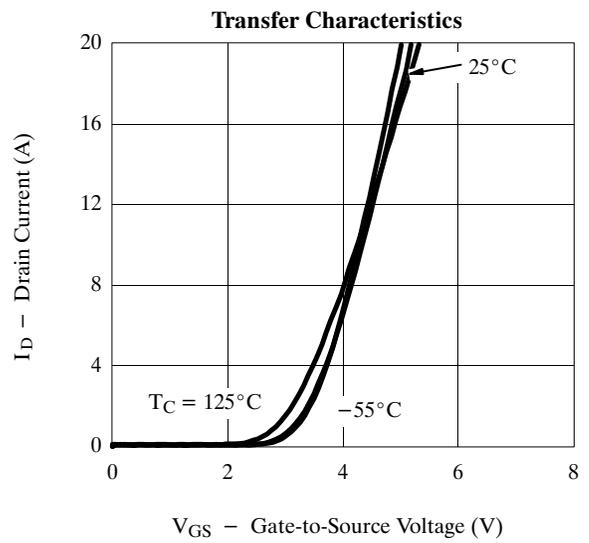
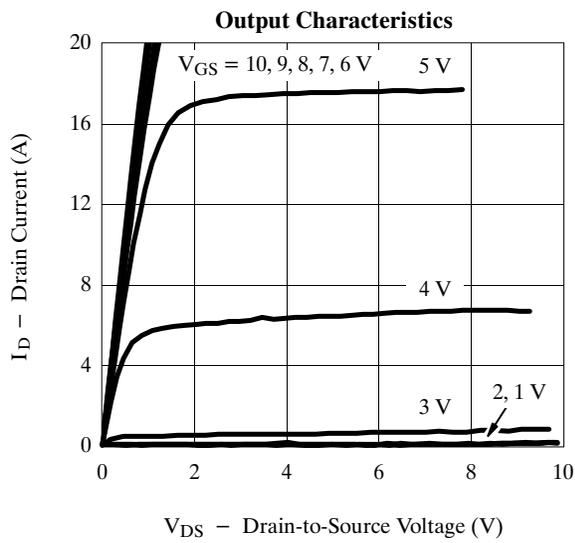
Notes

a. Guaranteed by design, not subject to production testing.

b. Pulse test; pulse width, duty cycle.

Typical Characteristics (25°C Unless Noted)

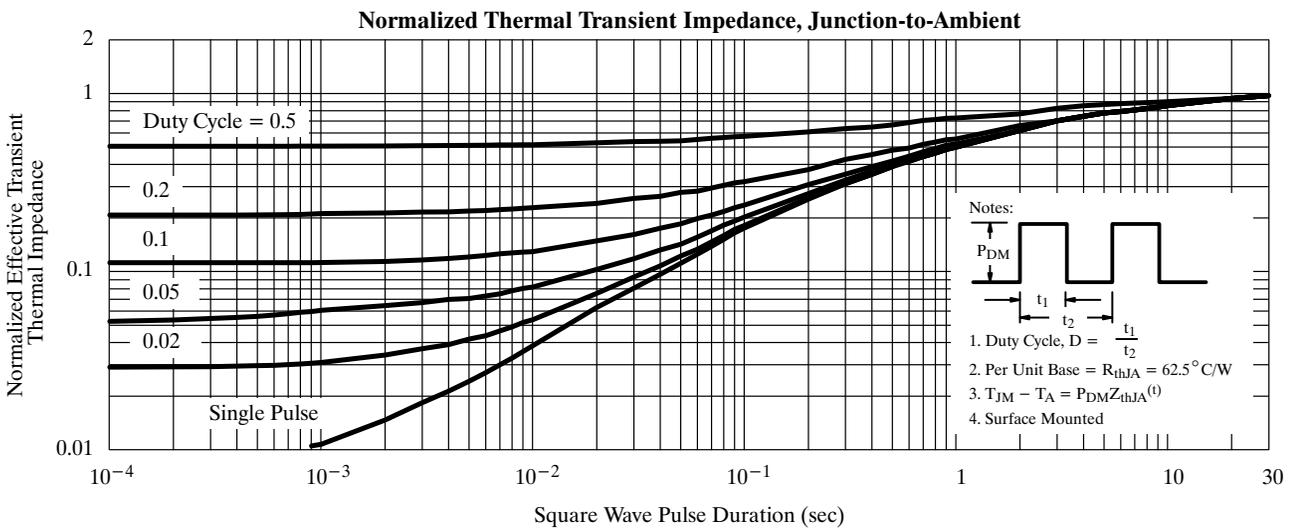
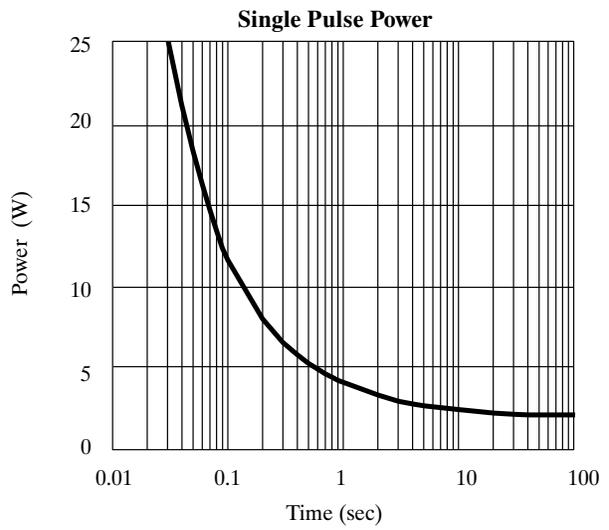
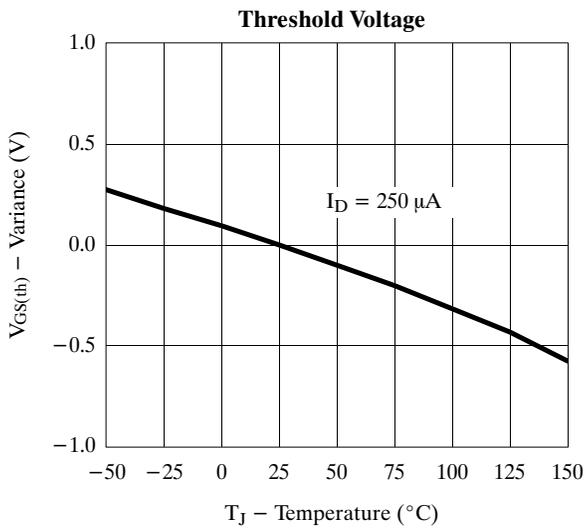
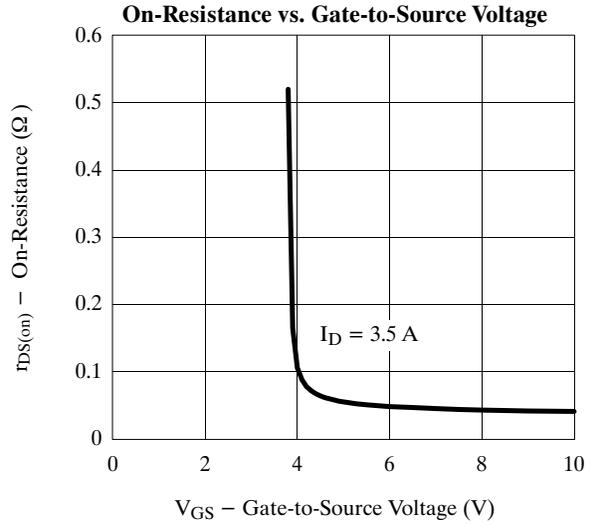
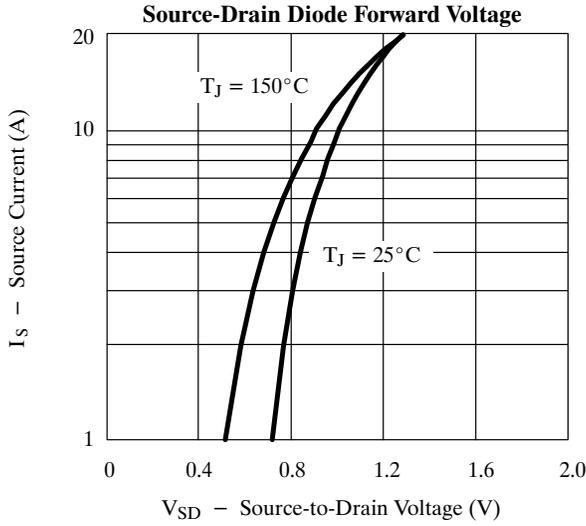
N-Channel



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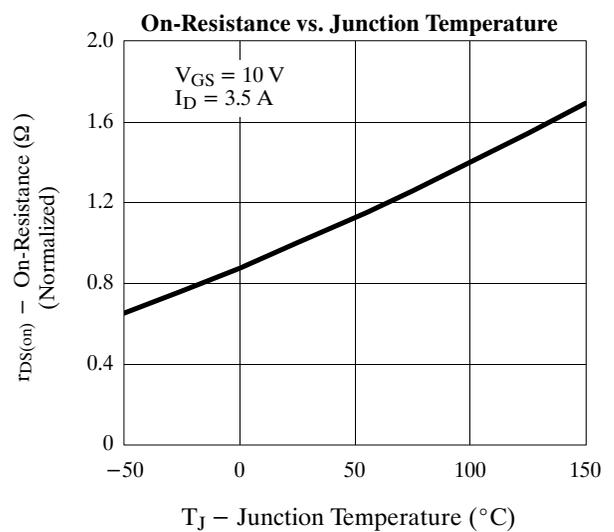
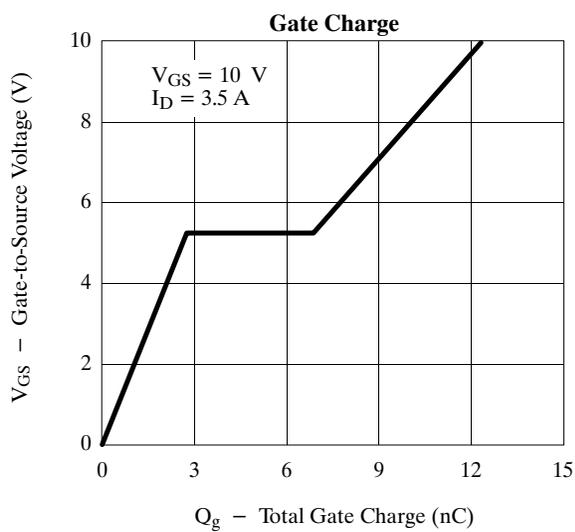
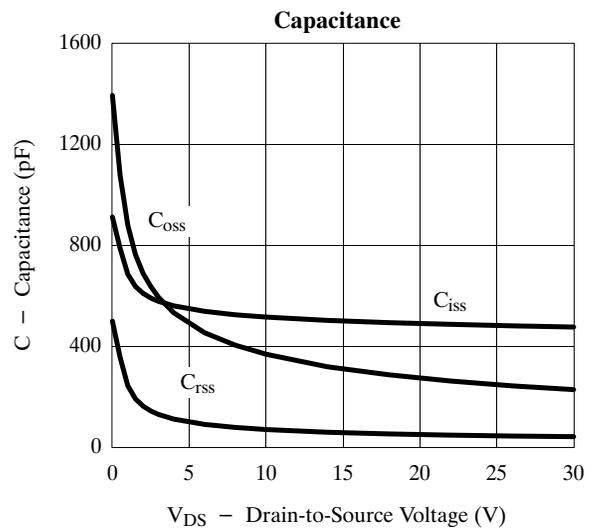
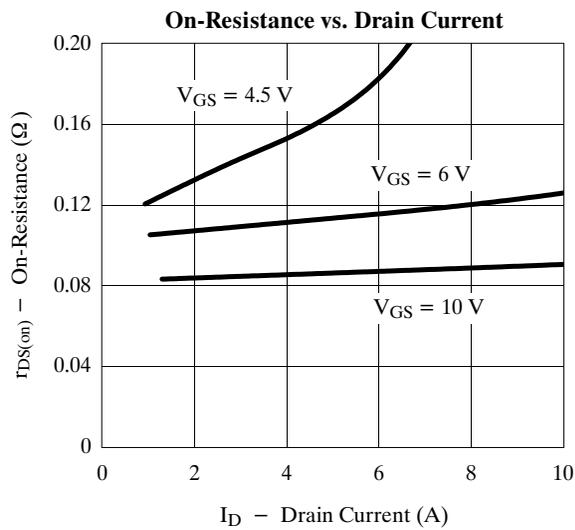
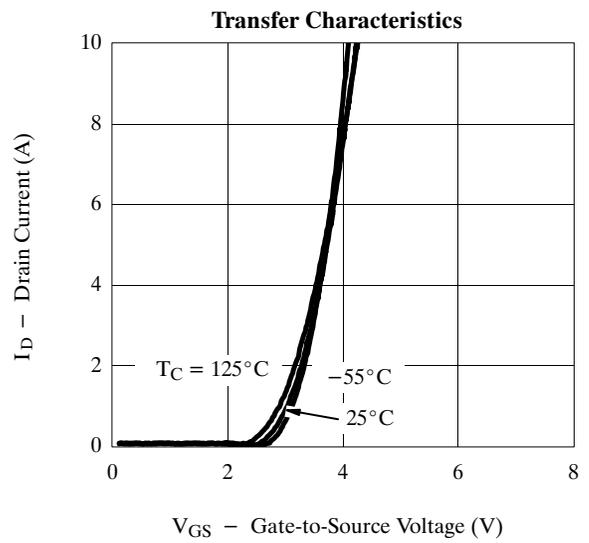
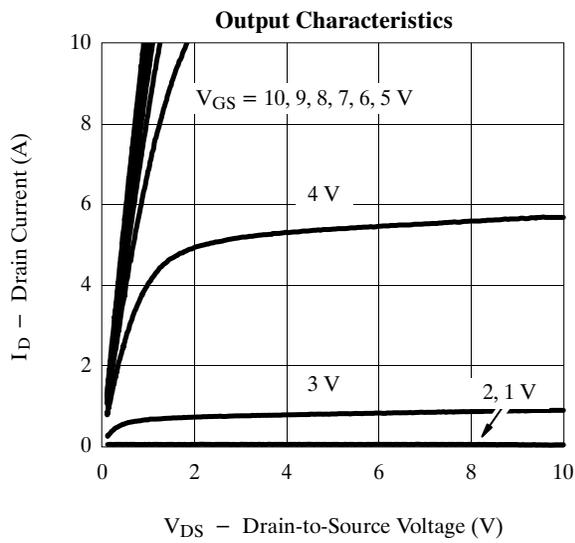
Typical Characteristics (25°C Unless Noted)

N-Channel



Typical Characteristics (25°C Unless Noted)

P-Channel



Typical Characteristics (25°C Unless Noted)

P-Channel

